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AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claim 9 (previously presented): A delay line comprising:

a multilayer structure formed by laminating a plurality of dielectric layers;

a transmission line formed on a dielectric layer embedded in the multilayer structure;

a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and

a capacitance disposed on the multilayer structure and connected in parallel to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is adjustable.

Claim 10 (original): A delay line according to claim 9, wherein said capacitance is provided by electrodes formed on respective ones of said dielectric layers.

Claim 11 (original): A delay line according to claim 9, wherein said capacitance is provided by a variable capacitor.

Claim 12 (original): A delay line according to claim 9, wherein said capacitance is provided by a varicap diode.

Claim 13 (previously presented): A delay line comprising:

a multilayer structure formed by laminating a plurality of dielectric layers;

a transmission line formed on a dielectric layer embedded in the multilayer structure;

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a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and

a capacitance disposed on the multilayer structure and connected in parallel to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is provided by a diode.

Claim 14 (original): A delay line according to claim 13, wherein said diode is a varicap diode.

Claims 16 and 17 (canceled)